

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/810,203	KANG ET AL.	
Examiner		Art Unit		Page 1 of 1
David S. Kim		2613		

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,923,219 A	07-1999	Ide et al.	330/308
*	B	US-5,955,921 A	09-1999	Ide et al.	330/254
*	C	US-6,018,407 A	01-2000	Hatakeyama et al.	398/209
*	D	US-6,081,362 A	06-2000	Hatakeyama et al.	398/210
*	E	US-6,292,058 B1	09-2001	Ide et al.	330/279
*	F	US-2002/0109075 A1	08-2002	Ono, Akihiko	250/214.00A
*	G	US-6,587,004 B2	07-2003	Ide, Satoshi	330/308
*	H	US-7,132,882 B2	11-2006	Chen et al.	330/9
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	EP 1006653 A3	06-2004	European Patent	IDE et al.	
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Nagahori, T. et al. "An analog front-end chip set employing an electro-optical mixed design on SPICE for 5-Gb/s/ch parallel optical interconnection." IEEE Journal of Solid-State Circuits, Vol. 36, No. 12, December 2001: 1984-1991.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.